DEEPX DX-M1 Al Accelerator

DX-M1 sets a new benchmark in energy-efficient Al computing, delivering **25 TOPS** (**200 eTOPS**) at under **5W**, significantly outperforming competitors in performance-per-watt.



DX-M1 Chip-Based Application Modules



DX-M1 M.2 LPDDR5×2

Type: Al Accelerator

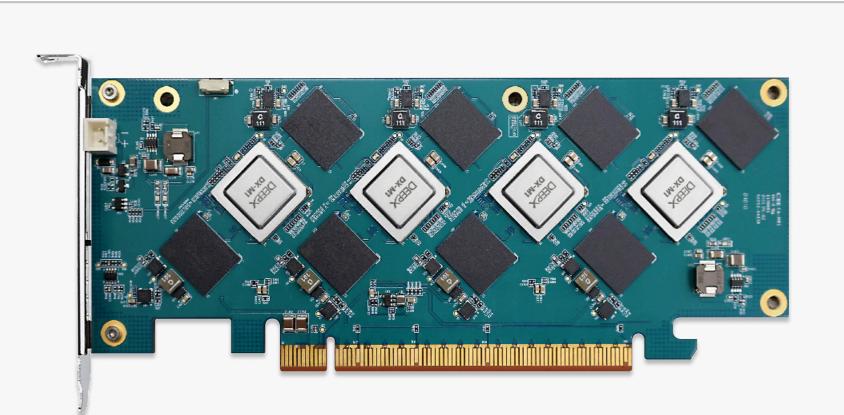
Al Perfermance: 25 TOPS / 3~5W Form Factor: M.2 M Key (22 × 80 mm)

Interface: PCle Gen.3 ×4

Memory: 4GB LPDDR5, QSPI 1Gbit NAND







DX-H1 PCle Card Quattro

Type: Al Accelerator

Al Perfermance: 100 TOPS / 20W

Form Factor: PCle Card (167 × 66.4mm)

Interface: PCle Gen.3 (4×4×4×4 Bifurcation)
Memory: 16GB LPDDR5, QSPI 1Gbit NAND





Key Features



Outstanding Power Efficiency

Exceptional Al performance with reduced power consumption, enabling longer battery life for your products and greater energy savings for data centers. (Butter-Proof)



Dedicated DRAM Design

Engineered for simultaneous multimodel processing, our dedicated DRAM ensures smooth, highperformance Al execution—even when running several models at once.



Universal CPU Compatibility

Designed for seamless integration with any host CPU architecture, enabling faster deployment, simplified development, and full compatibility of existing infrastructure.



Leading Cost Efficiency

Minimizes reliance on costly on-chip SRAM, delivering high performance with a dramatically lower bill of materials—bringing powerful Al accessible across more devices.

Al Performance

AI Model	Input Resolution	Accuracy (mAP) FP32 GPU	Accuracy (mAP) *IQ8 DX-M1	FPS DX-M1	NPU Power (W) DX-M1	FPS/W DX-M1
YOLOv5m	640×640	45.08	45.07	236	3.16	74.75
YOLOv7e6	640×640	55.58	55.45	19.49	2.50	7.81
YOLOv8x	640×640	53.63	53.12	49.03	3.17	15.48
YOLOv8I	640×640	52.57	52.01	93	3.60	25.32
YOLOv8m	640×640	50.11	49.56	135	2.76	48.32
YOLOv9c	640×640	52.86	52.36	47.89	2.83	16.95



DEEPX Headquarters

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Apply - DX TechBridge Kit

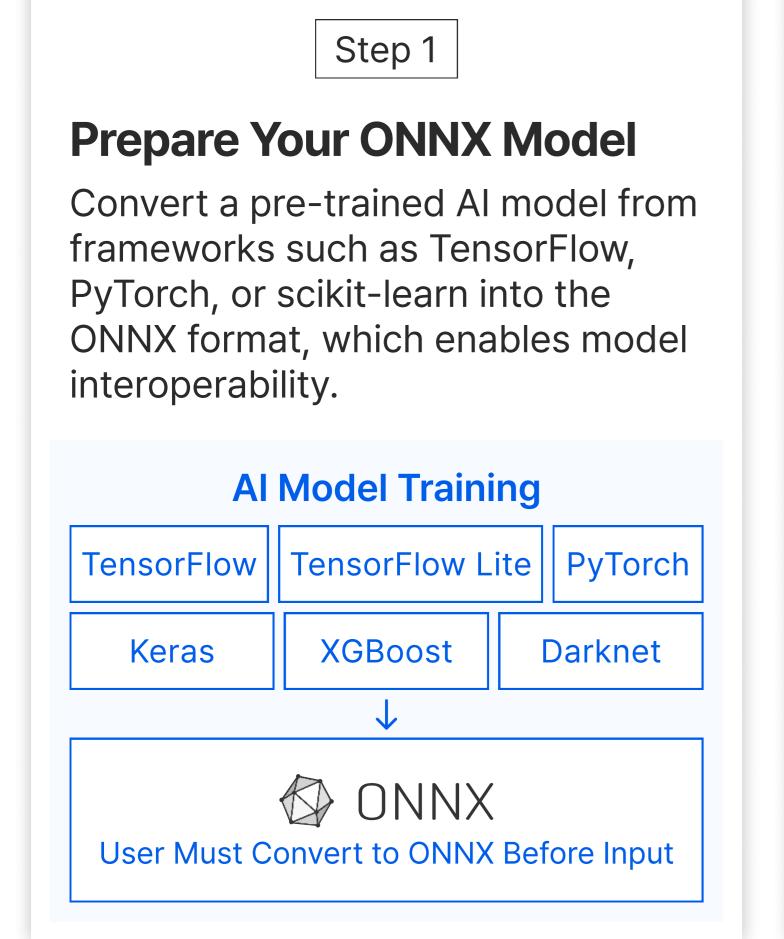
Sales Support - sales@deepx.ai

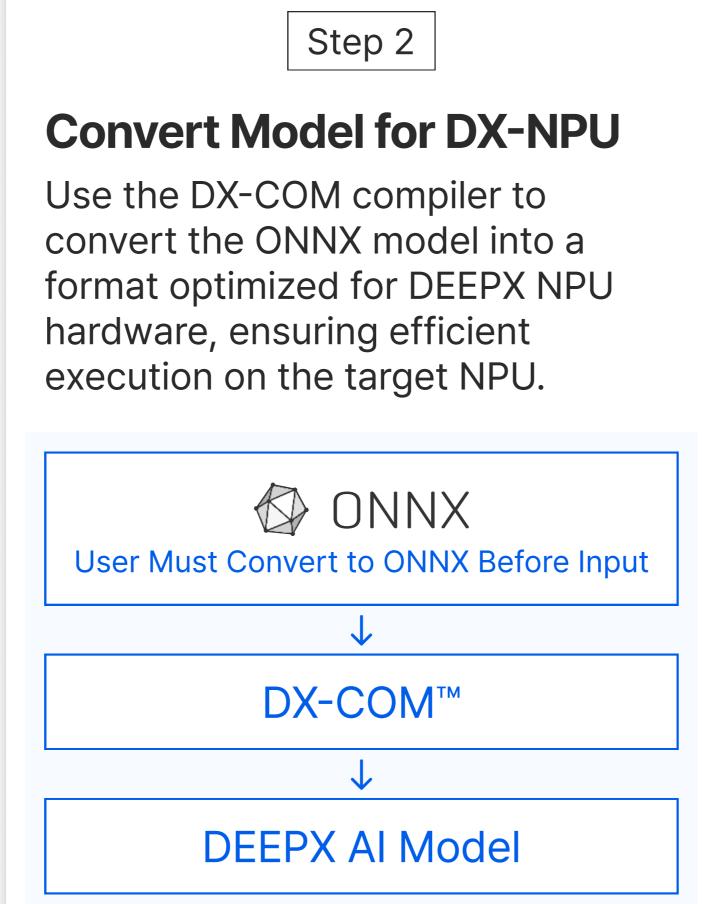
DXNN® SDK

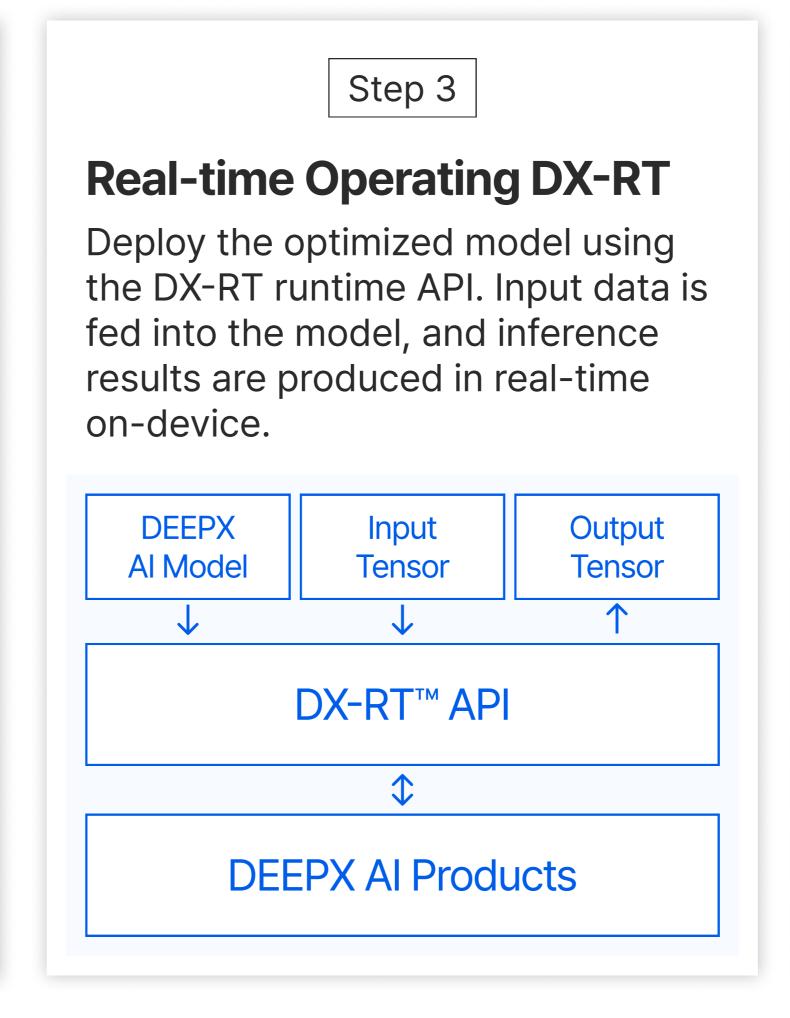
DXNN® (DEEPX Neural Network) SDK streamlines Al deployment on DEEPX NPUs by integrating versionaligned tools for compilation, optimization, simulation, and inference. For efficient development, it's offered as the DX-AS (All Suite), a fully integrated and optimized package.

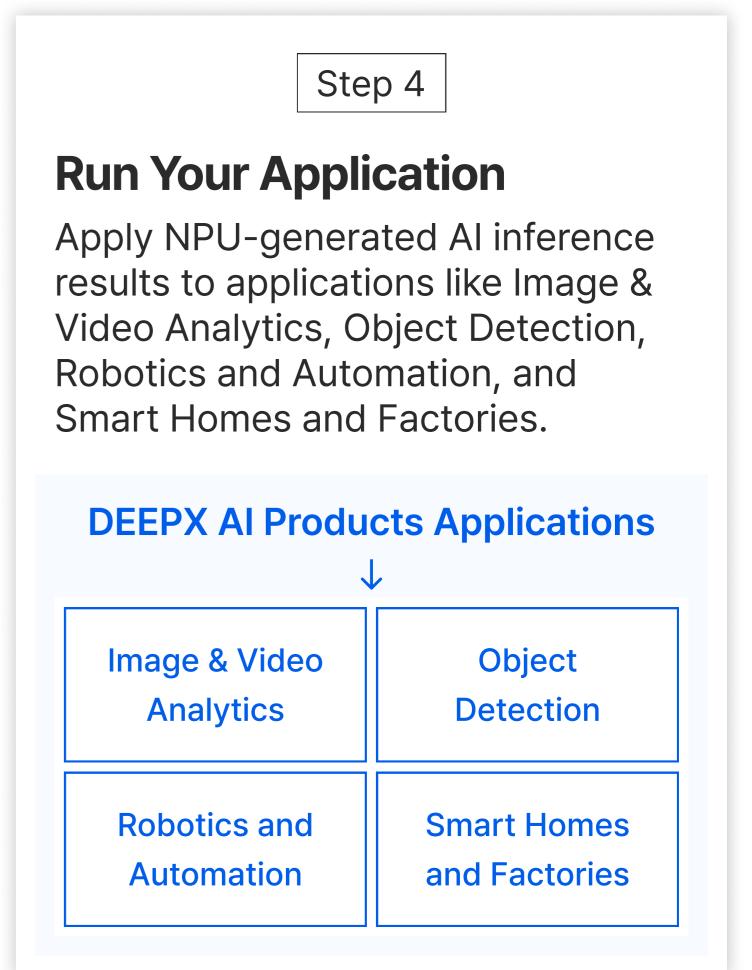


How It Works: 4-Step Al Deployment with DXNN® SDK









DXNN® Full Stack Architecture

DXNN® Full Stack Architecture streamlines Al model deployment onto DEEPX products using its two-stage Al Model Compile and Runtime Environments.

